

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jong-Soo CHOI, et al.

Serial No.: 10/748,868 Examiner: Beveridge, Rachel E.

Filed: December 29, 2003 Group Art Unit: 1725

Confirmation No.: 3708

For: PRINTED CIRCUIT BOARD WHICH CAN BE CONNECTED WITH
PIN CONNECTOR AND METHOD OF MANUFACTURING THE
PRINTED CIRCUIT BOARD

Date: December 5, 2006

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

In the Examiner's Statement of Reasons for Allowance, the Examiner recited portions of the allowed independent claims and stated that the prior art failed to teach or suggest either alone or in combination the recited portions. The applications note that it is a well-known tenet of patent law that each allowed patent claim stands alone. Further, although the Examiner has indicated at least one reason for allowance, there are other reasons that claims are allowable. In other words, the Examiner has not recited all of the reasons for allowance, and there are reasons for allowability in addition to those given by the Examiner.

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Respectfully submitted,
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